AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

LISTING OF CLAIMS:

Claims 1-15 cancelled.

16. (New) A method for transferring data via a data bus between a memory device and an electronic unit via a data bus, the memory device including a first memory area subdivided into pages and a second memory area, the pages being accessed using physical addresses of the pages, the first memory area being intended for storing data and the second memory area containing the physical addresses of the pages of the first memory area, the method comprising:

transferring data between the memory device and the electronic unit; and

during the data transfer, autonomously transferring the physical addresses from the second memory area to the electronic unit.

17. (New) The method as recited in claim 16, wherein at a beginning of the data transfer, a starting address of the second memory area is transmitted to the electronic unit.

18. (New) The method as recited in claim 16, wherein during the data transfer, data from the electronic unit is written into the memory device.

19. (New) The method as recited in claim 16, wherein during data transfer, data stored in the memory device is read by the electronic unit.

20. (New) The method as recited in claim 16, wherein the data bus is a PCI bus.

21. (New) The method as recited in claim 16, wherein the memory device is a main memory located on a motherboard of an electronic arithmetic-logic unit, and a plug-in card inserted into an expansion slot of the motherboard is provided as the electronic unit.

22. (New) The method as recited in claim 16, wherein the physical addresses are transferred to the electronic unit by DMA transfer.

23. (New) A memory device, comprising:

a first memory area subdivided into pages; and

a second memory area, the first memory area being intended for data, and physical addresses of the pages of the first memory area being stored in the second memory area.

24. (New) A method of using a memory device, comprising:

providing a main memory located on a motherboard of an electronic arithmetic-logic unit, the main memory including a first memory area divided into pages; and

performing a data transfer using the main memory.

25. (New) A motherboard of an electronic arithmetic-logic unit, comprising:

a processor; and

a main memory coupled to the processor via a bus, the main memory including a first memory area divided into pages and a second memory area storing physical addresses of the pages of the first memory area.

26. (New) An electronic arithmetic-logic unit, comprising:

a motherboard; and

a main memory located on the motherboard, the main memory including:

a first memory area subdivided into pages, and

a second memory area, the first memory area being intended for data and physical addresses of the pages of the first memory area being stored in the second memory area.

27. (New) A system, comprising:

an electronic unit; and

a memory device connected to the electronic unit via a data bus, the memory device including:

a first memory area subdivided into pages; and

a second memory area, the first memory area being intended for data and physical addresses of the pages of the first memory area being stored in the second memory area.

28. (New) An electronic device, comprising:

an electronic arithmetic-logic unit;

a memory device, the memory device including a first memory area subdivided into pages, and a second memory area, the first memory area being intended for data and physical addresses of the pages of the first memory area being stored in the second memory area; and

an electronic unit;

wherein the electronic arithmetic-logic unit, the memory device, and the electronic unit are integrated in one component.

29. (New) A computer program for execution by an electronic arithmetic-logic unit, the arithmetic-logic unit including a memory device having a first memory area divided into pages and a second memory area storing physical addresses of the pages of the first memory area,

and an electronic unit, the program including program code which, when executed by the electronic arithmetic-logic unit, causes the electronic arithmetic-logic unit to perform the following:

transferring data between the memory device and the electronic unit; and

during the data transfer, autonomously transferring physical addresses from the second memory area to the electronic unit.

30. (New) A memory device storing program code for execution by an electronic arithmetic-logic unit, having a memory device and an elecronic unit coupled via a data bus, the memory device having a first memory area divided into pages, and a second memory area storing physical addresses of the pages of the first memory area, the program code, when executed by the arithmetic logic unit, causes the arithmetic-logic unit to perform:

transferring data between the memory device and the electronic unit; and

during the data transfer, autonomously transferring physical addresses from the second memory area to the electronic unit.